RESPONSE UNDER 37 C.F.R. § 1.116 **EXPEDITED PROCEDURE EXAMINING GROUP 2100**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor:

Khosro Khakzadi et al.

Appln. No.:

10/719,673

Filed

November 21, 2003

For

CHIP DESIGN COMMAND PROCESSOR

Docket No.:

03-1862/L13.12-0251

Group Art Unit: 2179

Examiner: Eric A Wiener

ELECTRONICALLY FILED ON October 8, 2007

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action dated July 27, 2007. Please amend the above-identified application as follows.